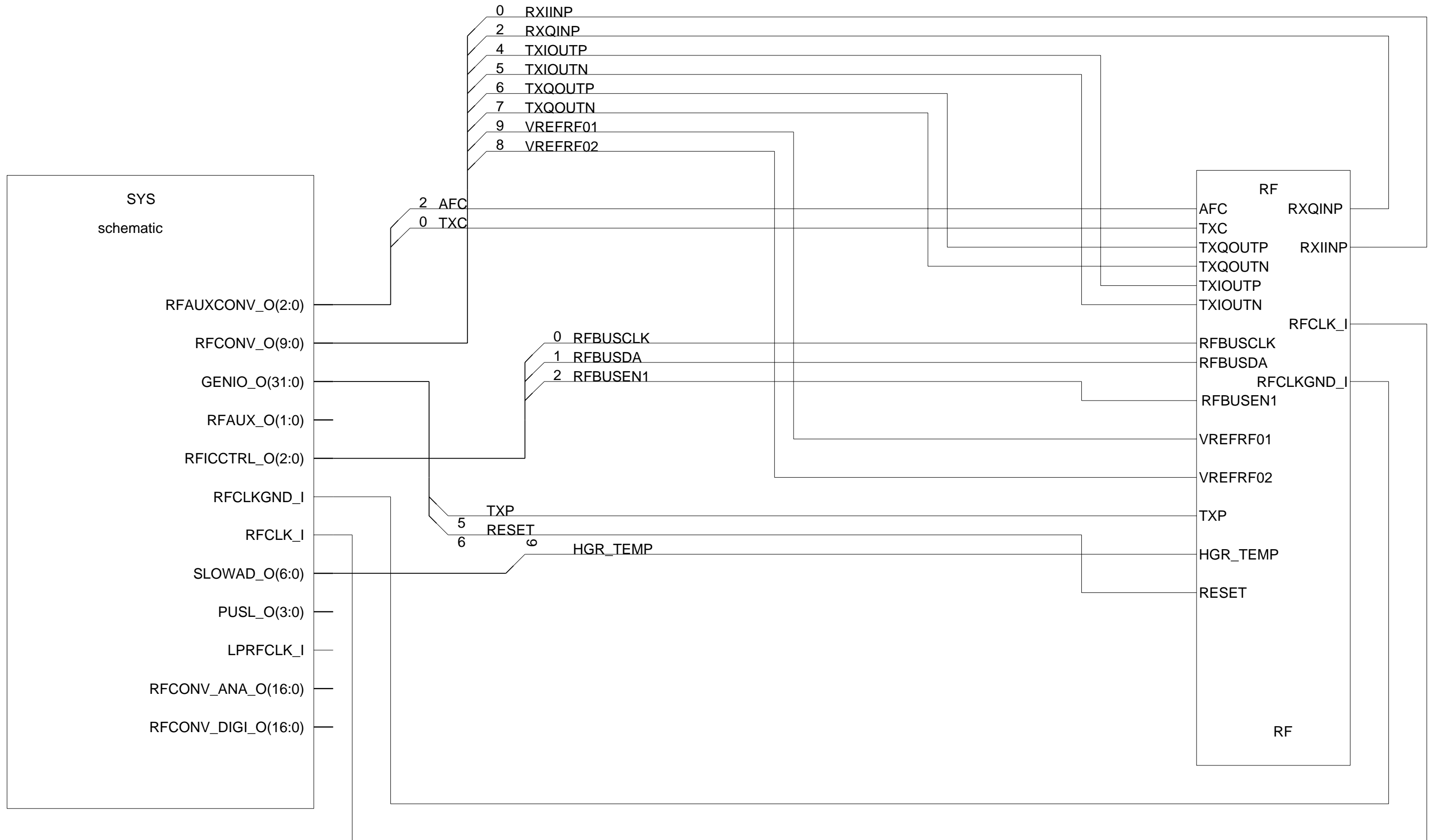
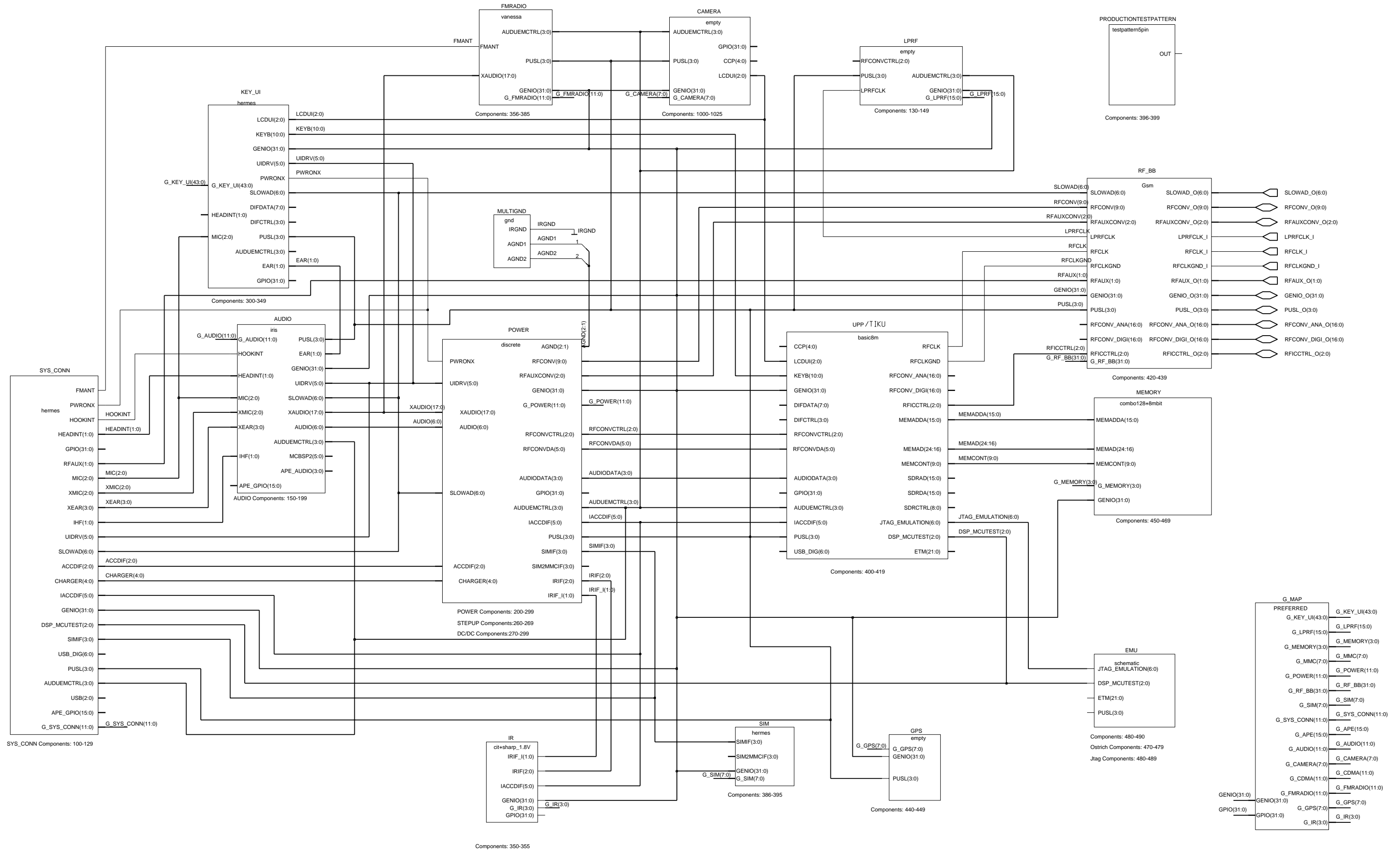


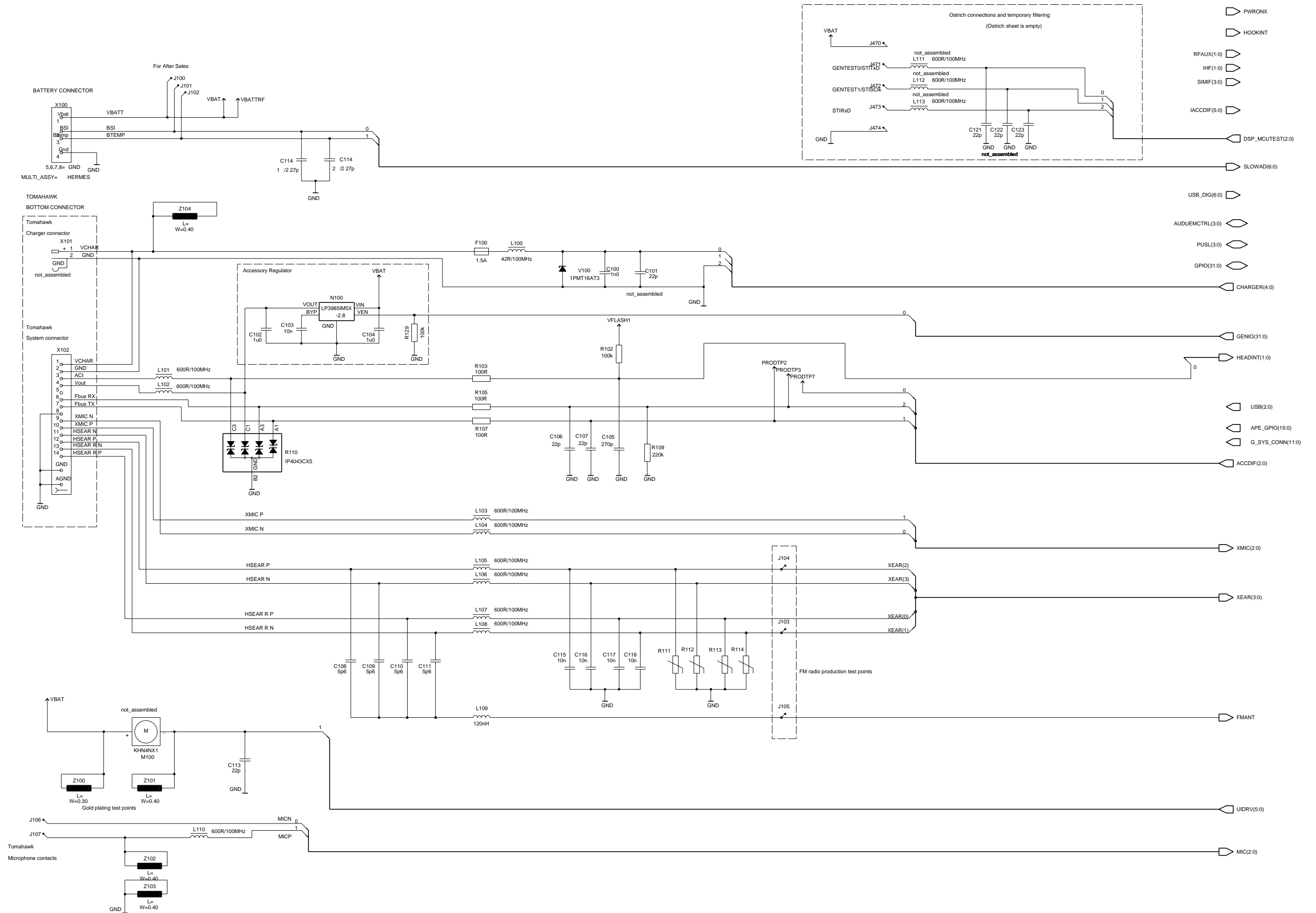
RM9



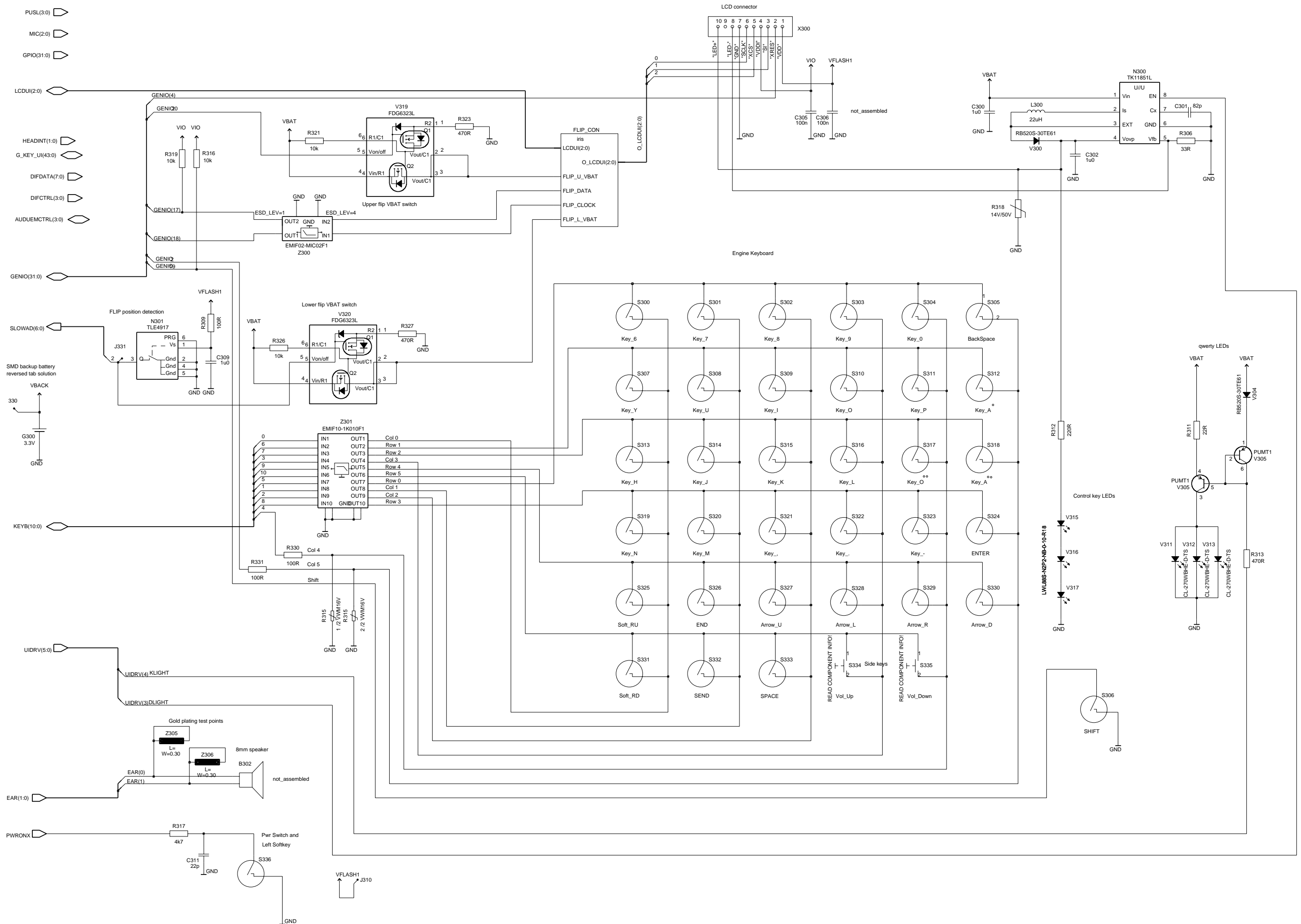
System



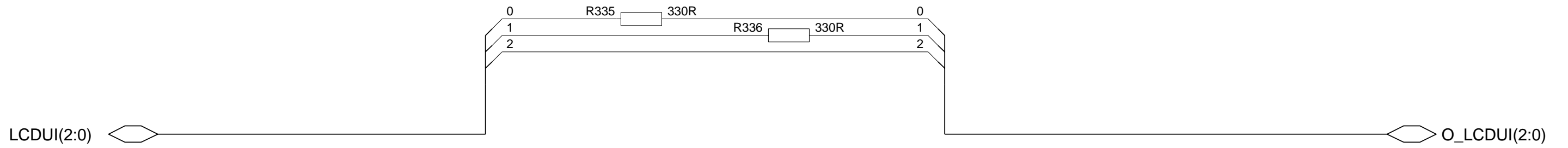
System Connector



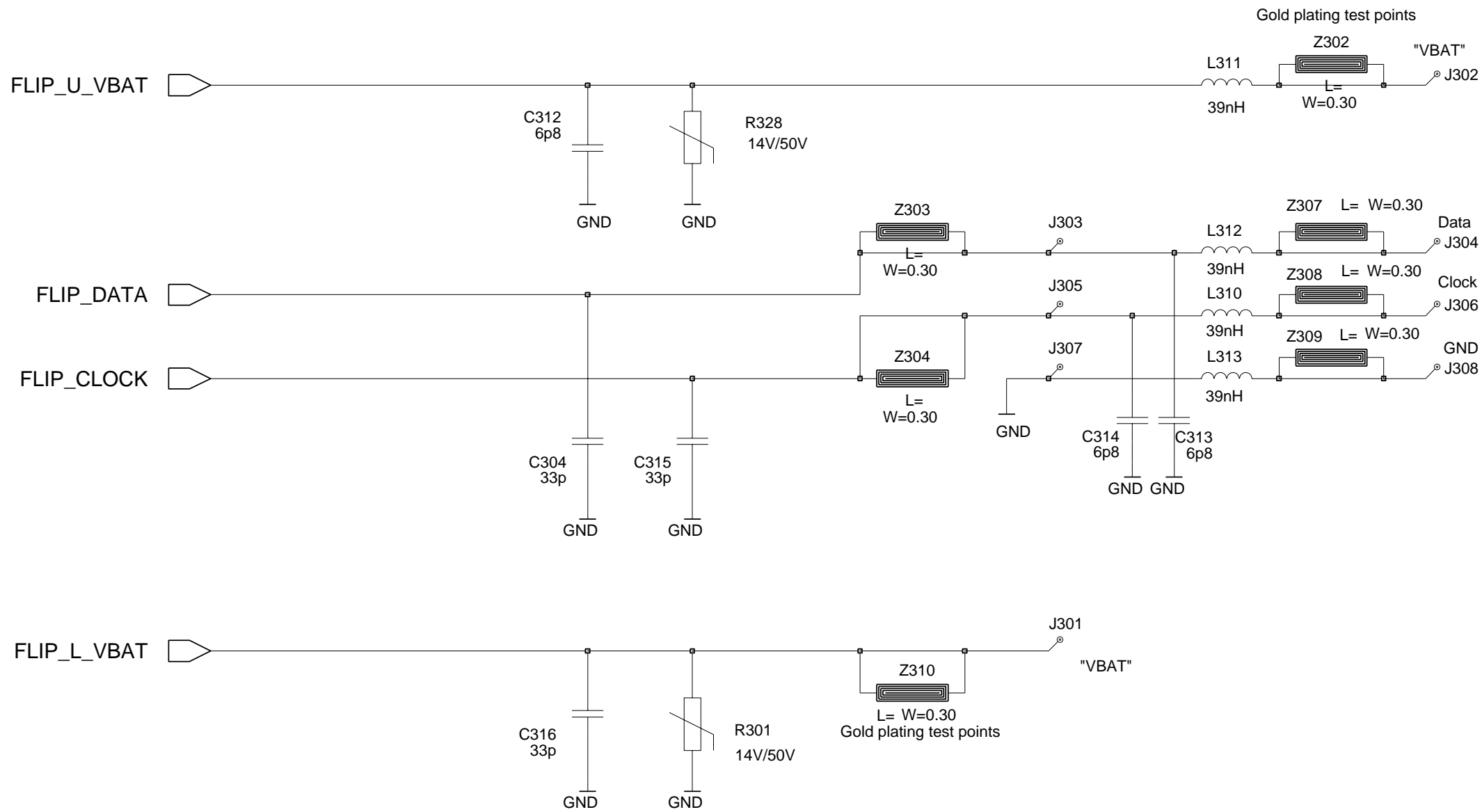
Key UI



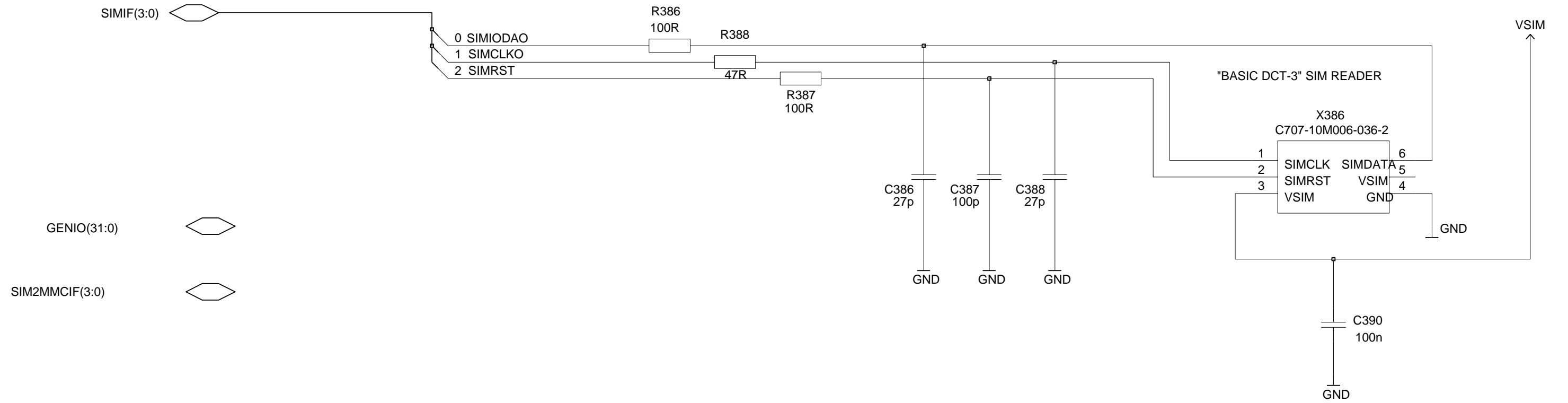
Flip Connector



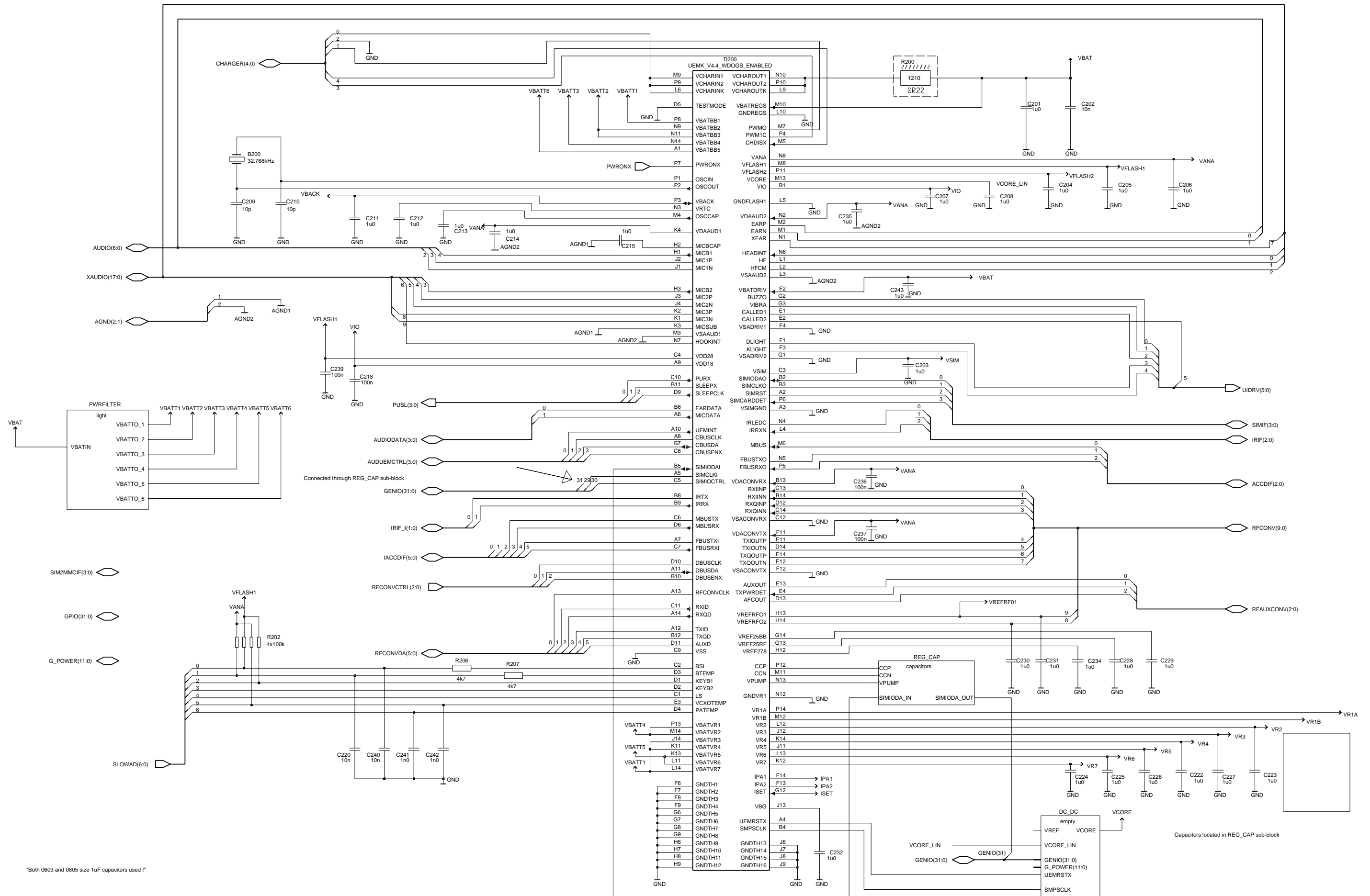
Flip connections



SIM



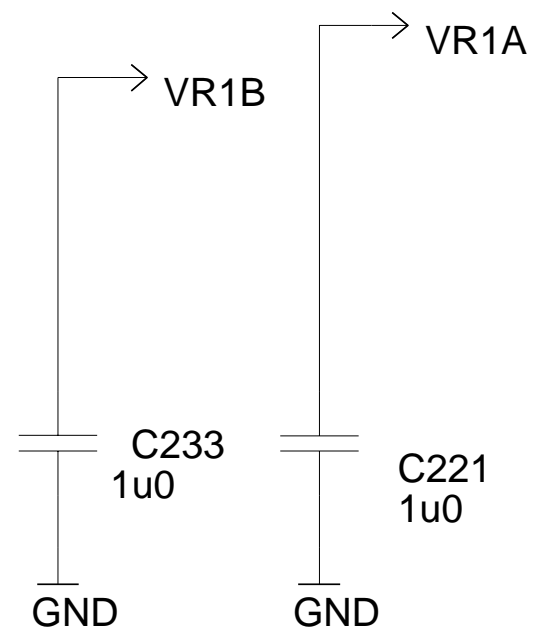
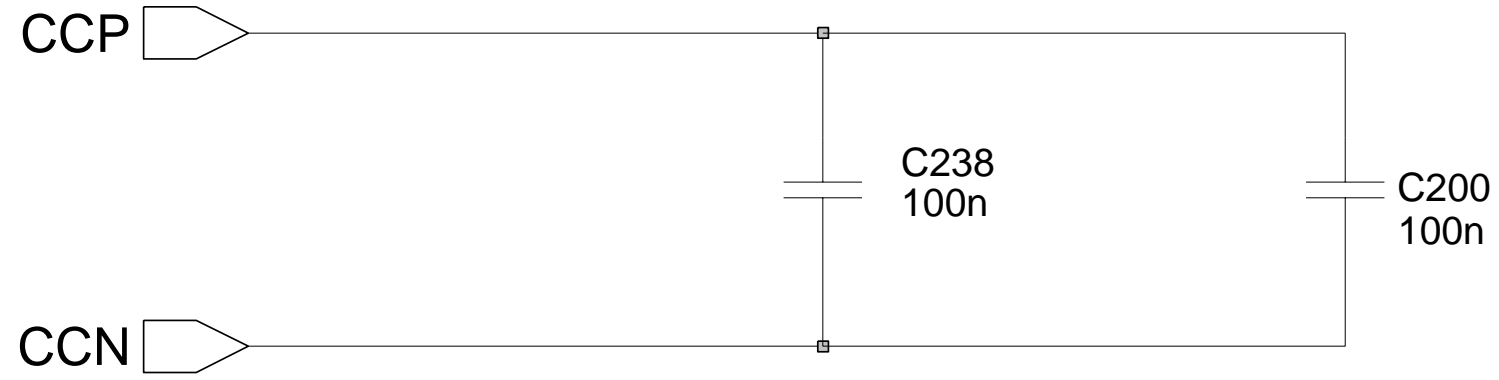
Power



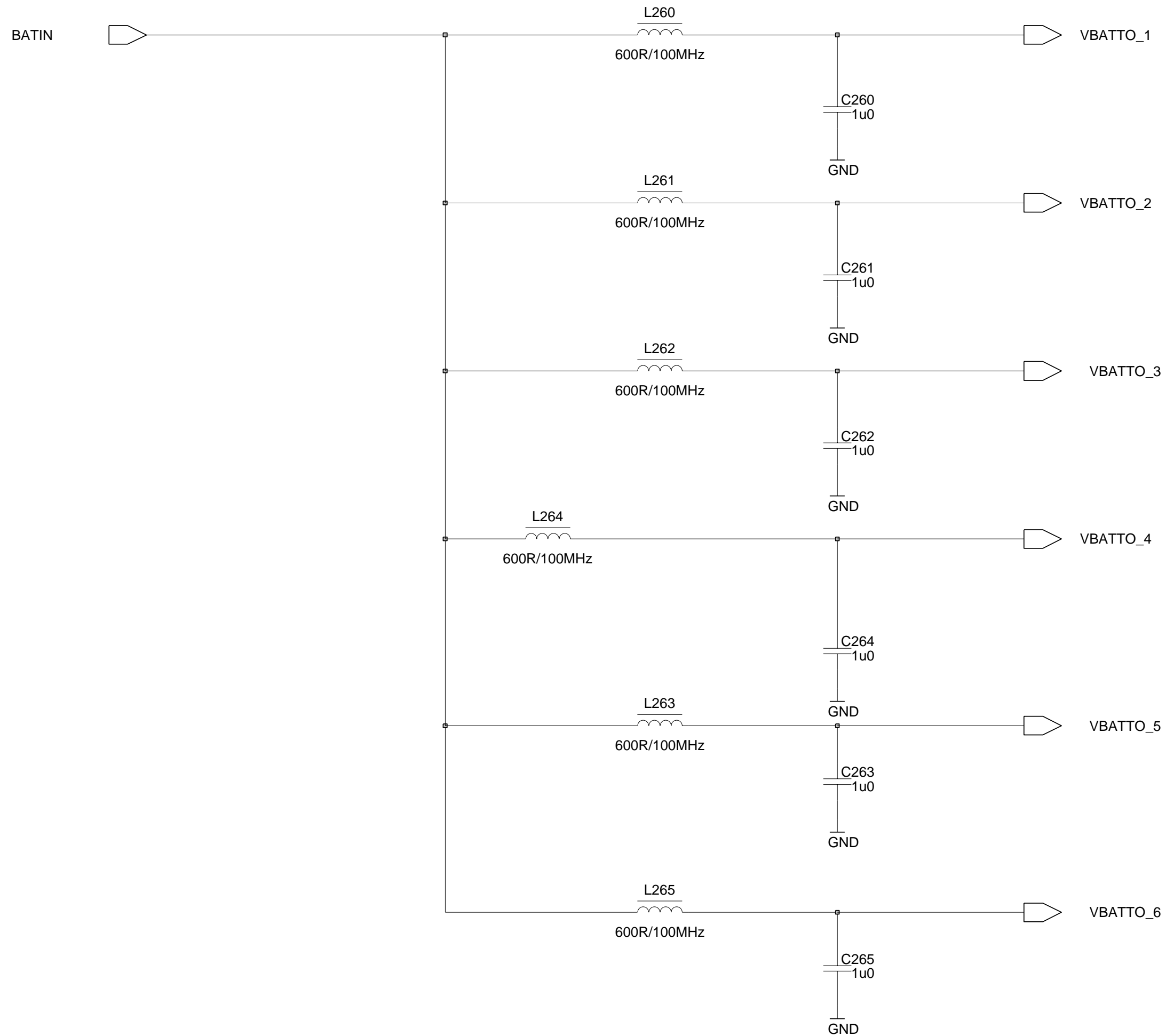
"Both 0603 and 0805 size 1uF capacitors used!"

Used refs in REG_CAP: C238, C200, C219, C233, C221 and R205

Reg Cap

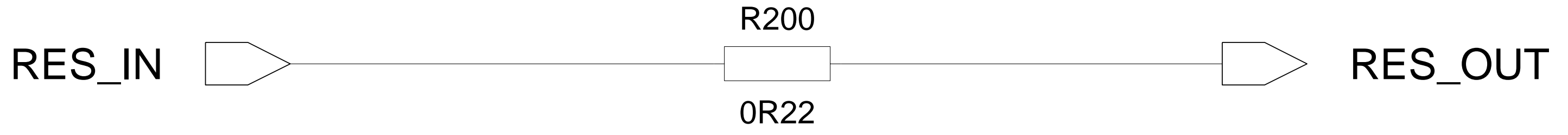


Powerfilter

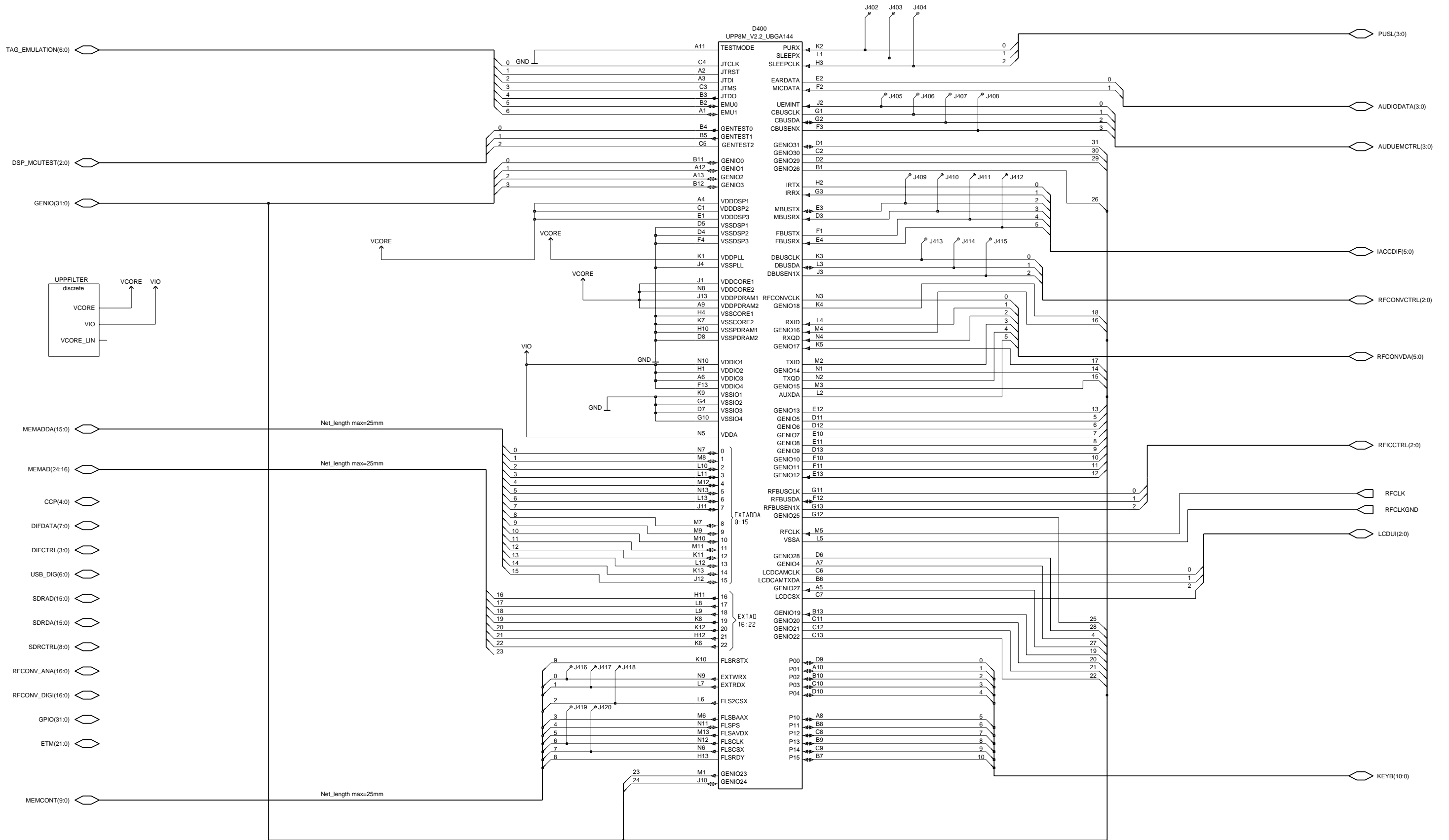


R200

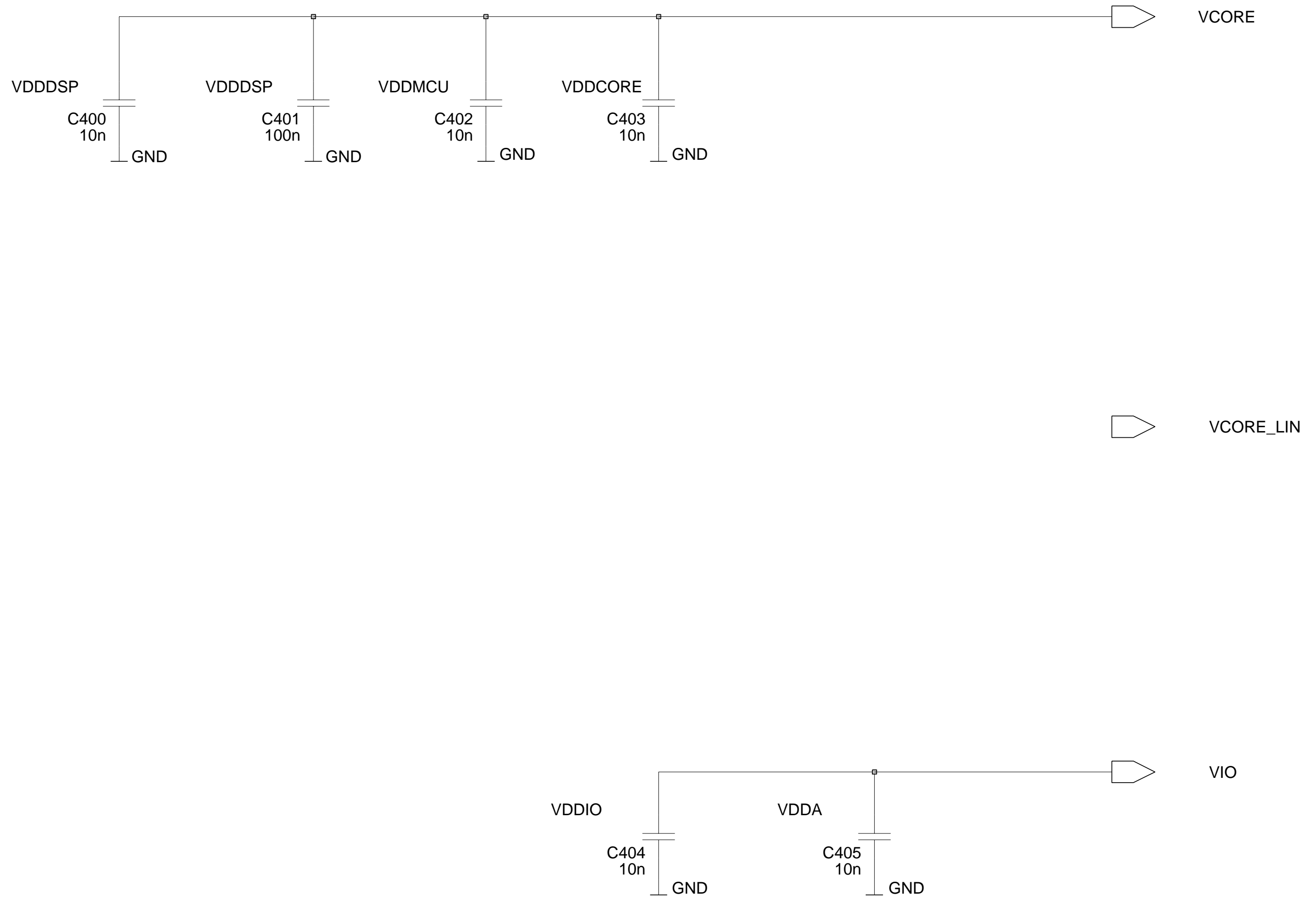
Will be changed to reference number in viewpoint



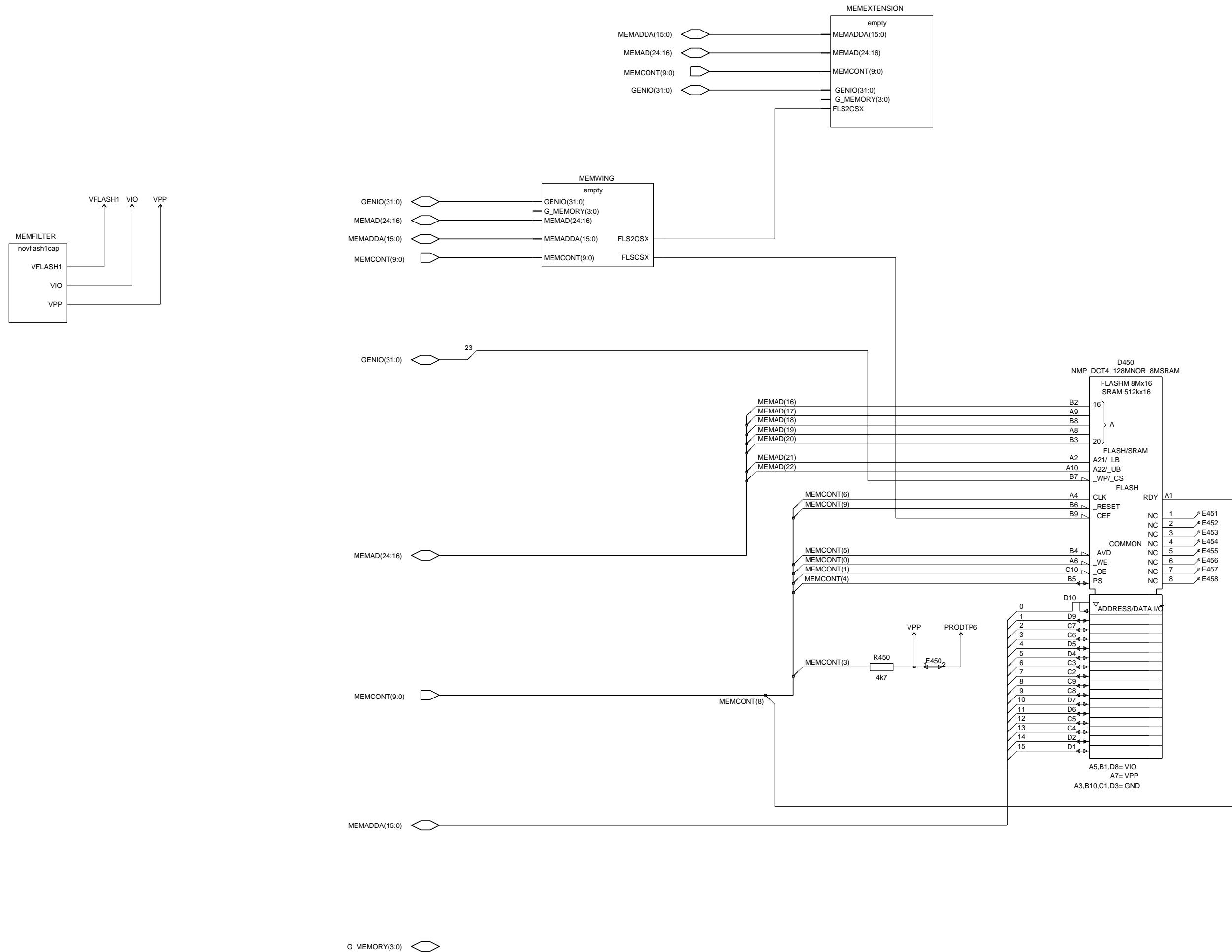
UPP




UPP Filter




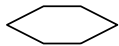
Memory

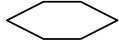


Memory Wing

MEMADDA(15:0) 

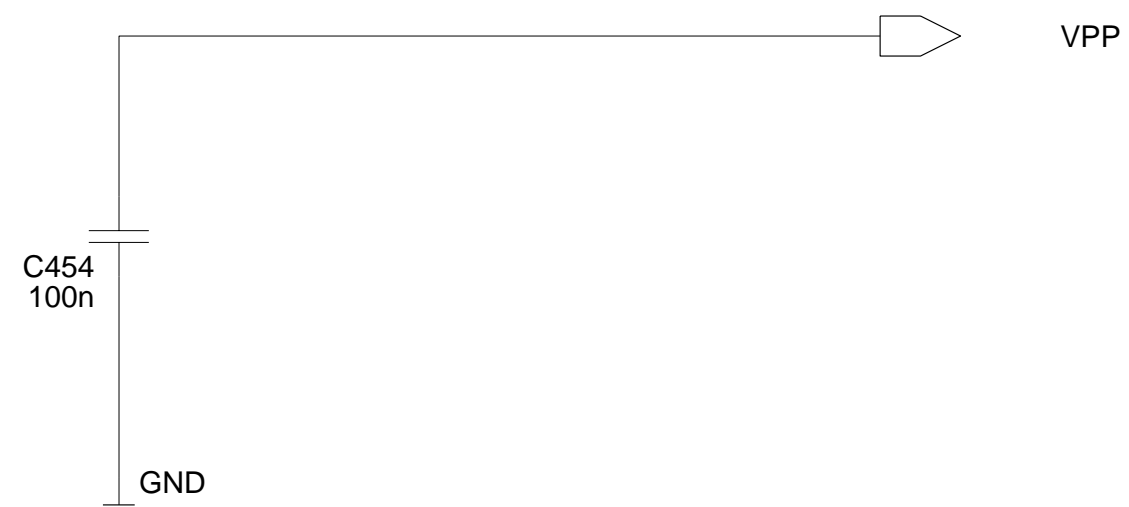
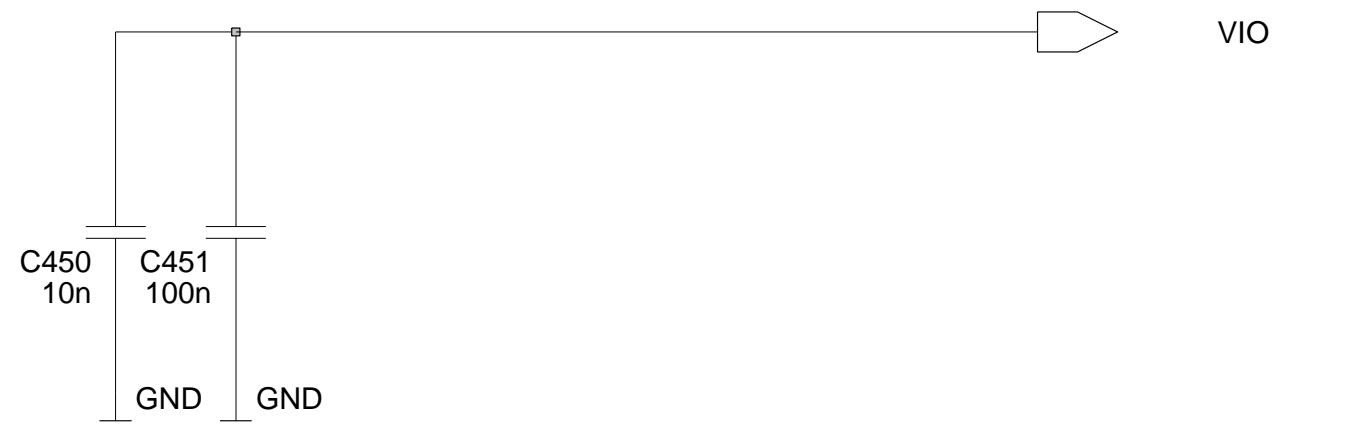
MEMAD(24:16) 

GENIO(31:0) 

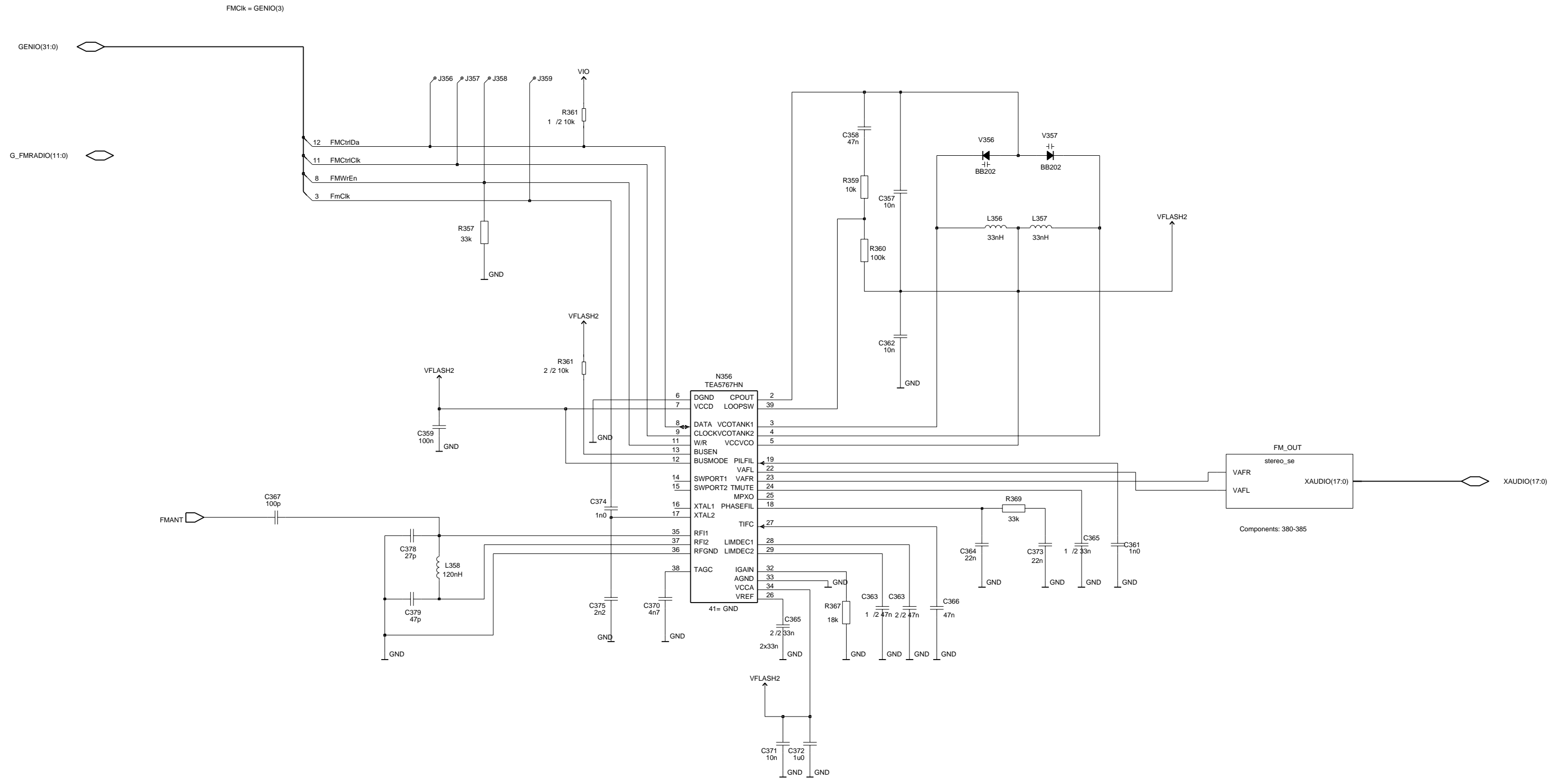
G_MEMORY(3:0) 



Memory Filter



FM Radio



AUDUEMCTRL(3:0)

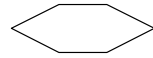
PUSL(3:0)

Notice:
 C374 (1n0) and C375 (2n2) are configured for 32kHz reference clock
 If reference clock is 6.5MHz, use C374 (3p9) and C375 (10p)

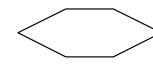
FM Out



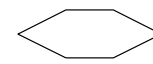
G_CAMERA(7:0)



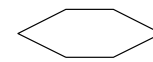
AUDUEMCTRL(3:0)



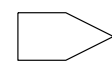
LCDUI(2:0)



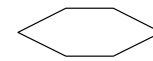
GENIO(31:0)



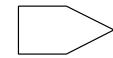
CCP(4:0)



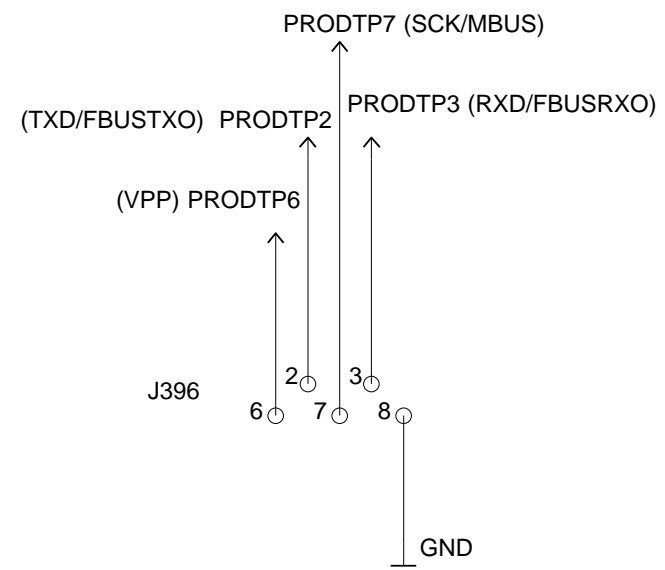
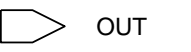
GPIO(31:0)



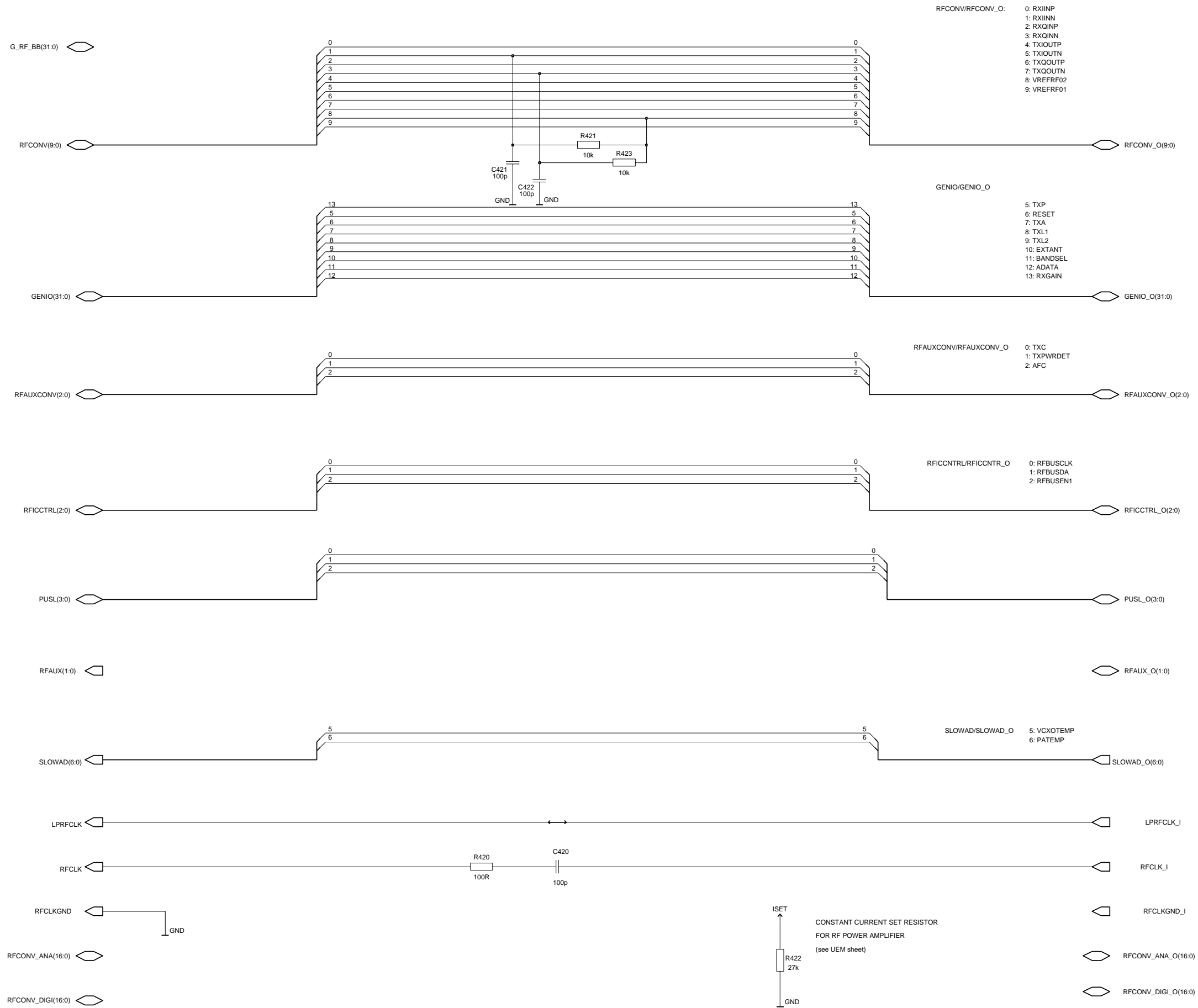
PUSL(3:0)



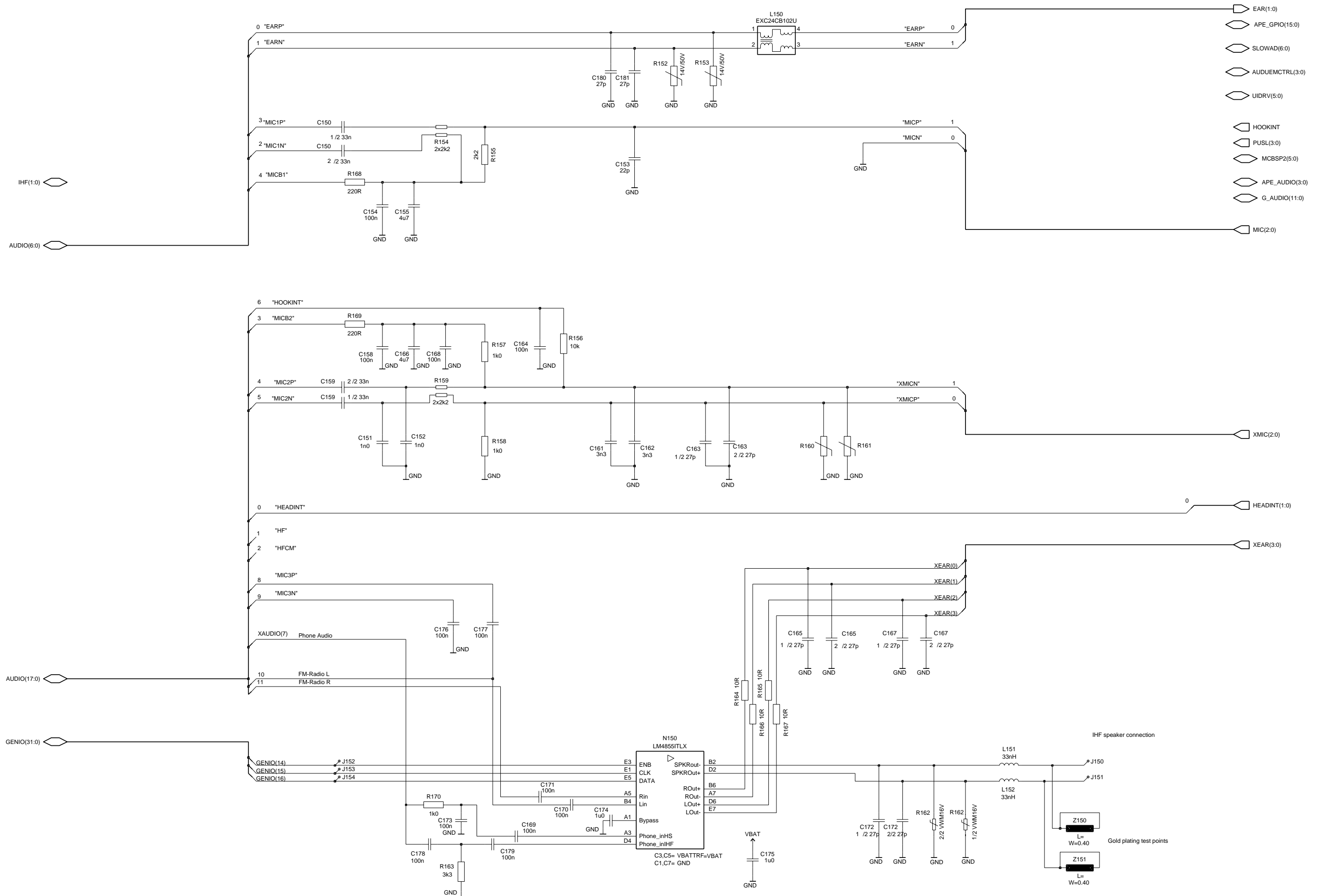
Production Testpattern



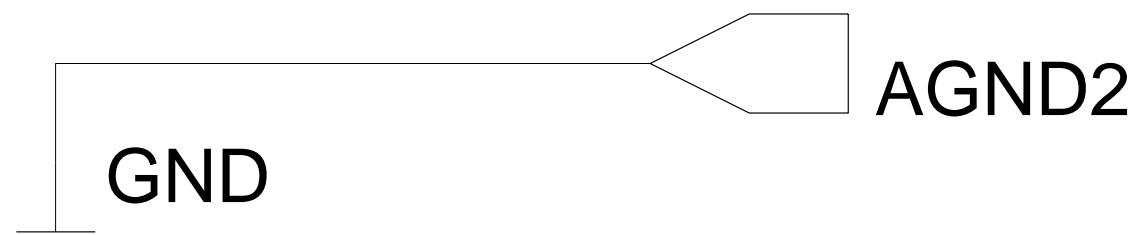
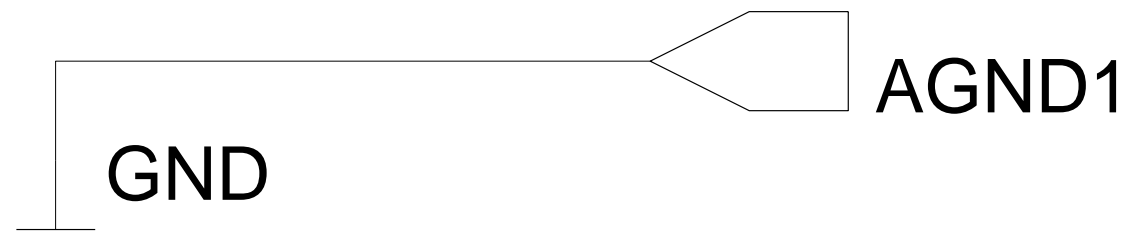
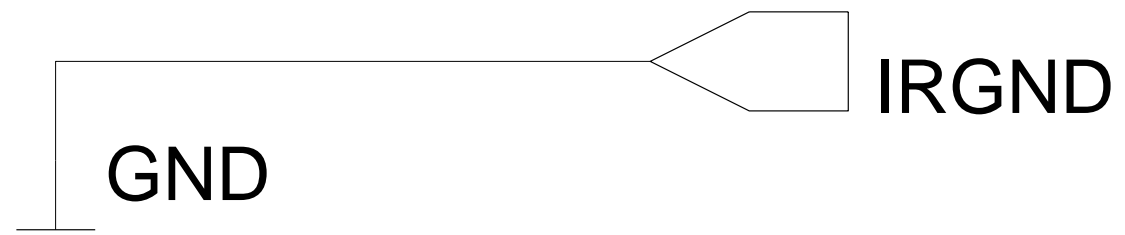
RF and BB



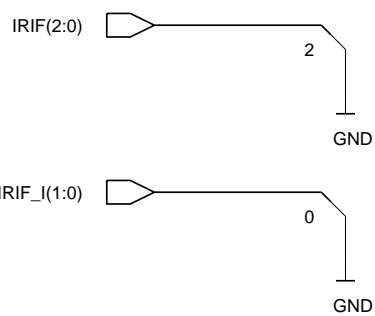
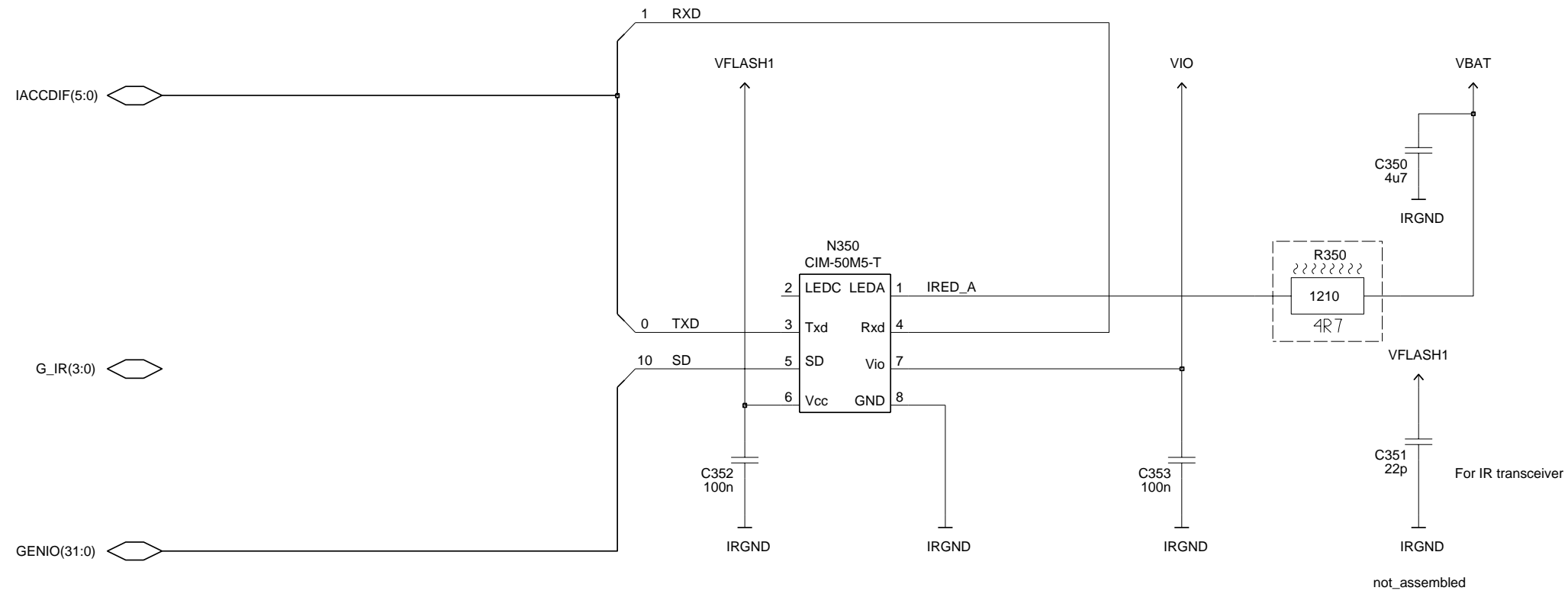
Audio



Multi GND



IR



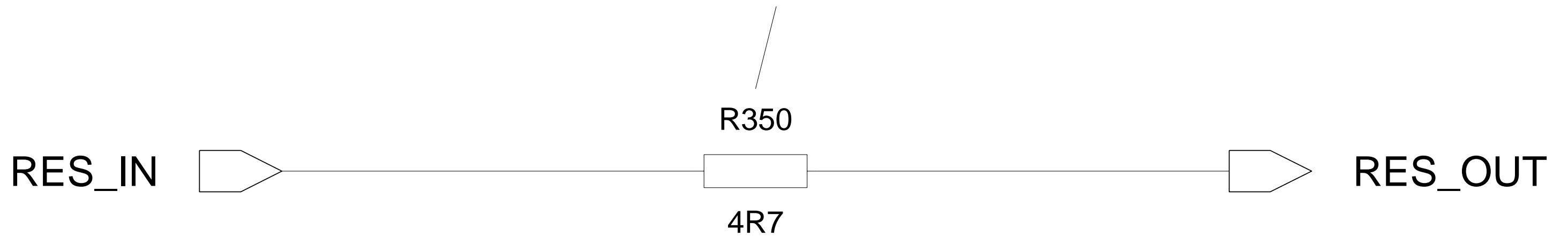
UEM IR level shifters are ground, when 1.8V IR is used!

Used referenses

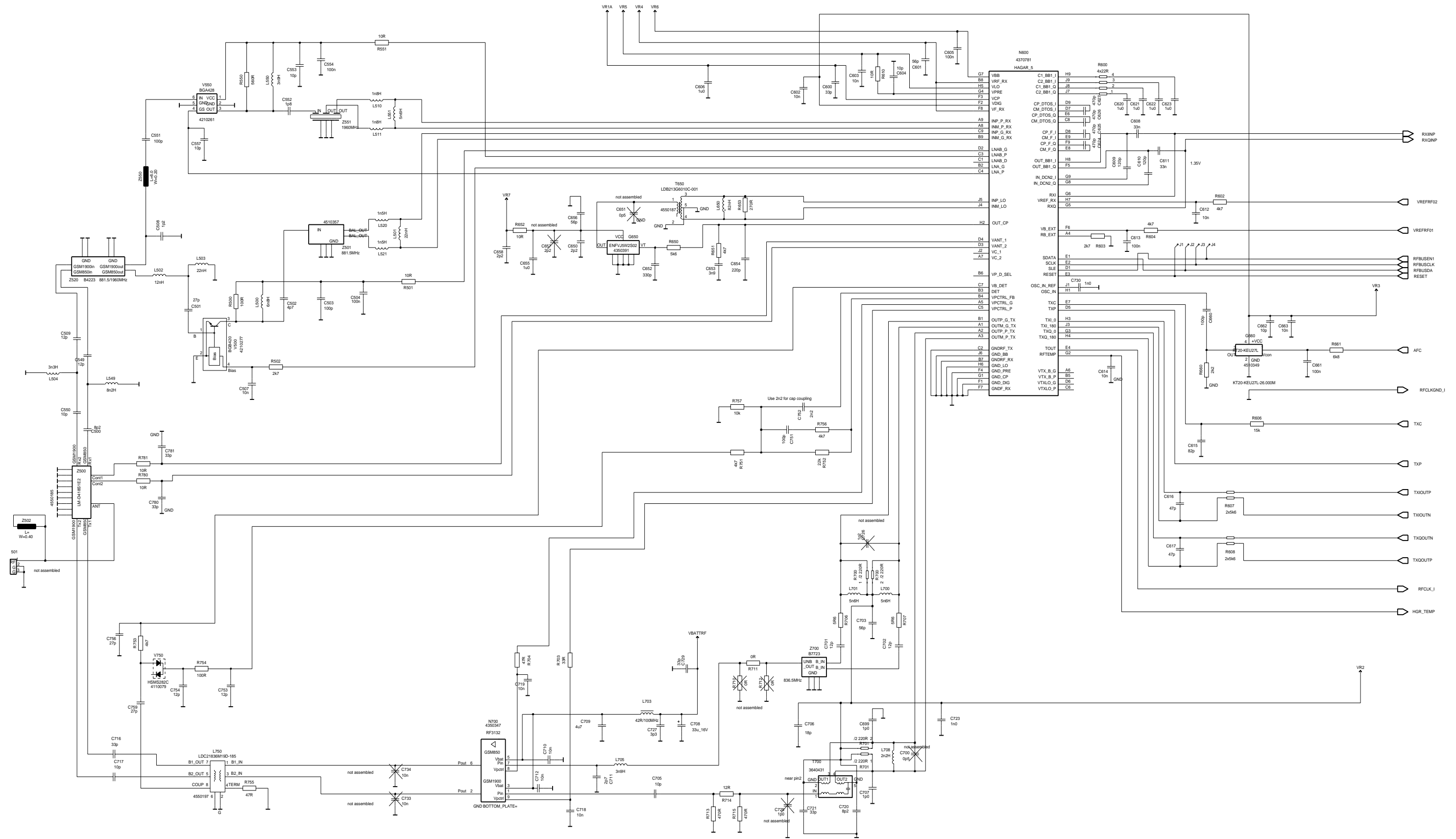
- C 350 - 353
- N 350
- R 350

R350

Will be changed to reference number in viewpoint

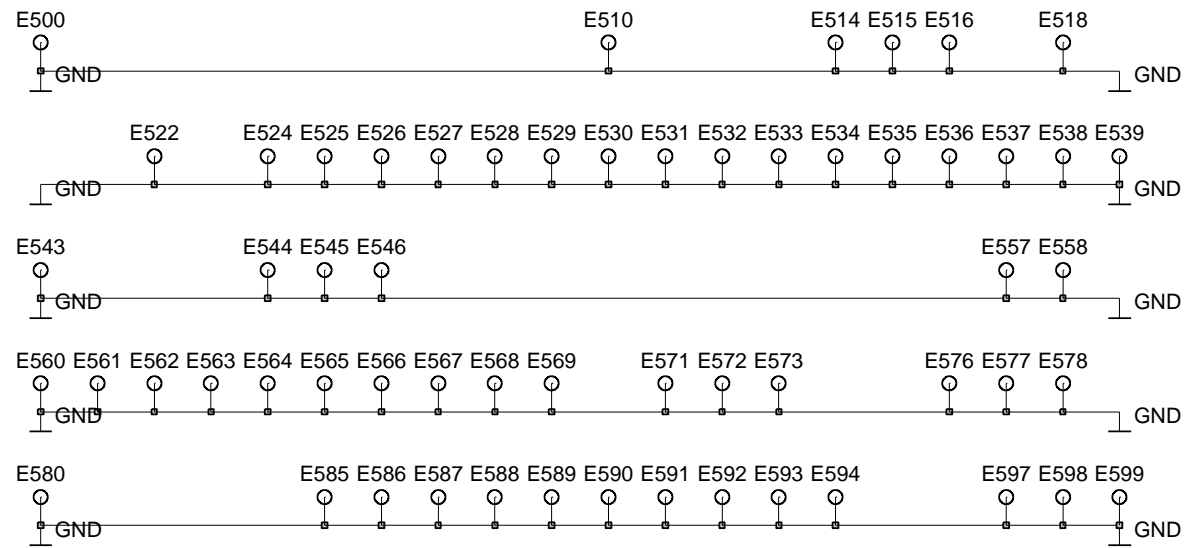


RF

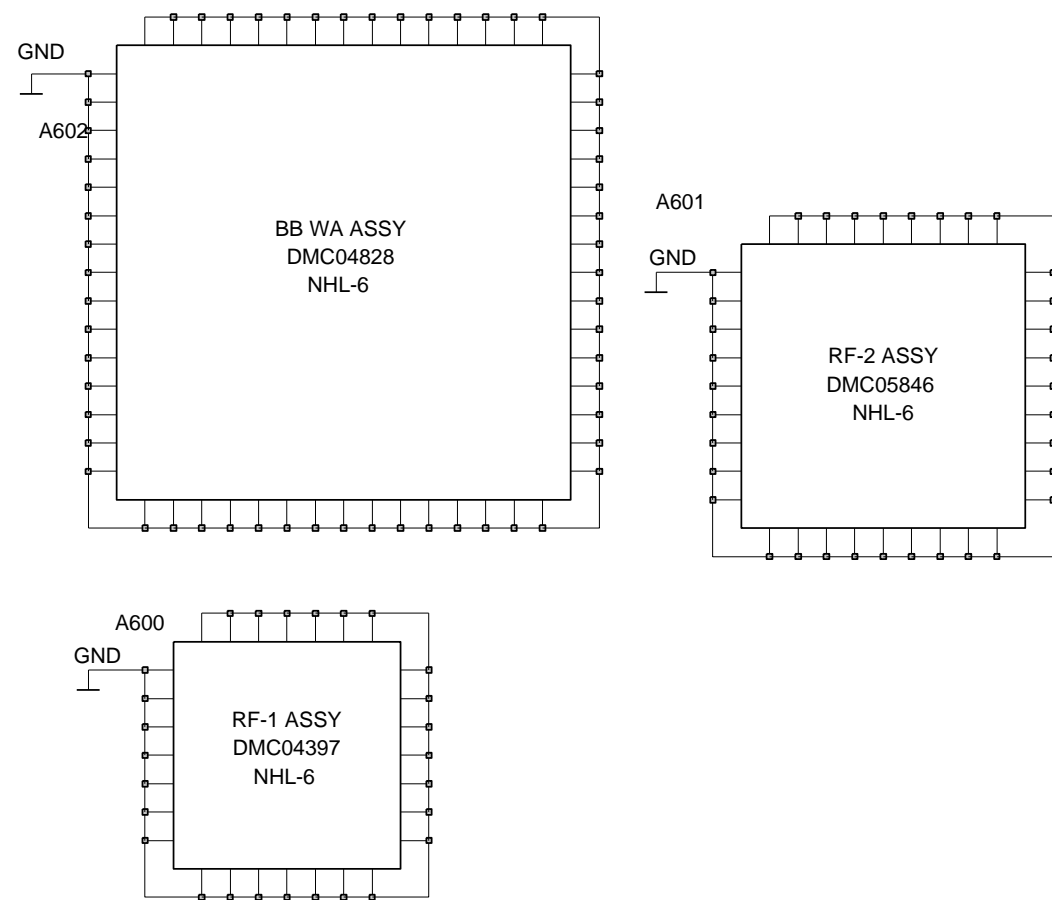
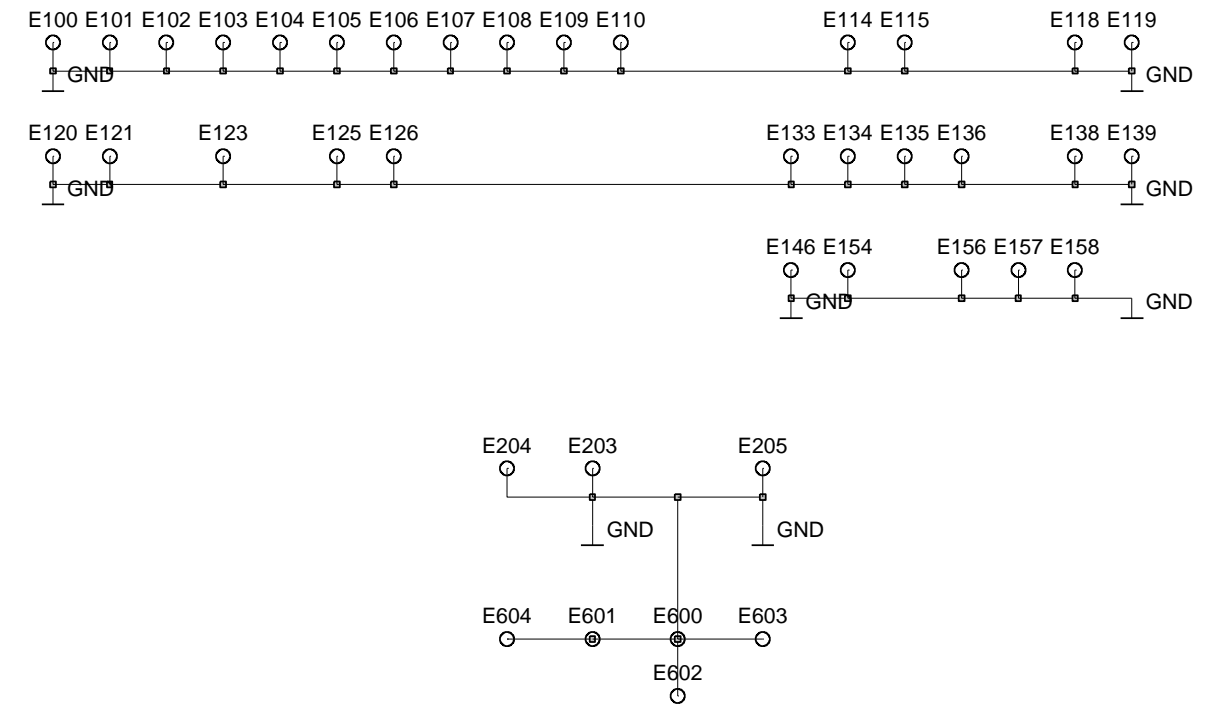


Shields

RF GLOBAL GND HOLES



SYSTEM GLOBAL GND HOLES



Assembly Values Diagram Top

